

Notice of Allowability	Application No.	Applicant(s)
	10/775,610	OTT, ARTHUR RICHARD
	Examiner Nelson D. Hernandez	Art Unit 2622

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. This communication is responsive to 10 February 2004.
2. The allowed claim(s) is/are 1-23.
3. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) All
 - b) Some* c) None
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

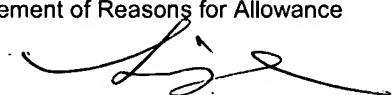
Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) hereto or 2) to Paper No./Mail Date _____.
 - (b) including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. Notice of References Cited (PTO-892)
2. Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. Information Disclosure Statements (PTO/SB/08),
Paper No./Mail Date _____
4. Examiner's Comment Regarding Requirement for Deposit
of Biological Material
5. Notice of Informal Patent Application
6. Interview Summary (PTO-413),
Paper No./Mail Date _____.
7. Examiner's Amendment/Comment
8. Examiner's Statement of Reasons for Allowance
9. Other _____.



LIN YE
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DETAILED ACTION

Allowable Subject Matter

1. **Claims 1-23** are allowed.
2. The following is a statement of reasons for the indication of allowable subject matter:

Regarding claim 1, the main reason for indication of allowable subject matter is because the prior art fails to teach or reasonably suggest, including all the elements of the present claim, a field effect transistor having a gate terminal coupled to said biased input, a source terminal coupled to ground potential, and a drain terminal coupled to an inhibiting input of said amplifier; and said drain terminal of said field effect transistor coupled to a source of voltage for bias thereof, such that when a signal is present on said input said field effect transistor turns OFF, which enables said tri-state amplifier so that said signal is passed through to said output bus.

Regarding claim 8, the main reason for indication of allowable subject matter is because the prior art fails to teach or reasonably suggest, including all the elements of the present claim, a field effect transistor having a gate terminal coupled to said biased input, a source terminal coupled to ground potential, and a drain terminal coupled to an inhibiting input of said amplifier; and said drain terminal of said field effect transistor coupled to a source of voltage for bias thereof, such that when a signal is present on said input said field effect transistor turns OFF, which enables said tri-state amplifier so that said signal is passed through to said output bus.

Regarding claim 14, the main reason for indication of allowable subject matter is because the prior art fails to teach or reasonably suggest, including all the elements of the present claim, a field effect transistor having a gate terminal coupled to said biased input, a source terminal coupled to ground potential, and a drain terminal coupled to an inhibiting input of said amplifier; and said drain terminal of said field effect transistor coupled to a source of voltage for bias thereof, such that when a signal is present on said input said field effect transistor turns OFF, which enables said tri-state amplifier so that said signal is passed through to said output bus.

Regarding claim 19, the main reason for indication of allowable subject matter is because the prior art fails to teach or reasonably suggest, including all the elements of the present claim, coupling a gate terminal of a field effect transistor to said biased input, coupling a source terminal thereof to ground potential, and coupling a drain terminal to an inhibiting input of said amplifier; coupling said drain terminal of said field effect transistor to a source of voltage for bias thereof; providing a signal on said input, which turns off said field effect transistor thereby enabling said tri-state amplifier so that said signal is passed through to said output bus.

Conclusion

3. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

De Langen, US 2006/0012411 A1. In fig. 5 teaches a driver amplifier to direct signals to a bus.

Whittaker et al., US Patent 5,418,935. See figs. 1 and 4 for structure related to a tri-state bus driver.

Whittaker et al., US Patent 5,086,427. See figs. 2, 5 and 8 for structure related to a tri-state bus driver.

Scott et al., US 2004/0228475 A1. In figs 6A and 6B teaches a general structure of a general structure related to an active diode bridge circuit using a tri-state amplifier providing an isolation barrier.

Contact

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Nelson D. Hernandez whose telephone number is (571) 272-7311. The examiner can normally be reached on 9:30 A.M. to 6:00 P.M..

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Lin Ye can be reached on (571) 272-7372. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Nelson D. Hernandez
Examiner
Art Unit 2622

NDHH
June 28, 2007



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